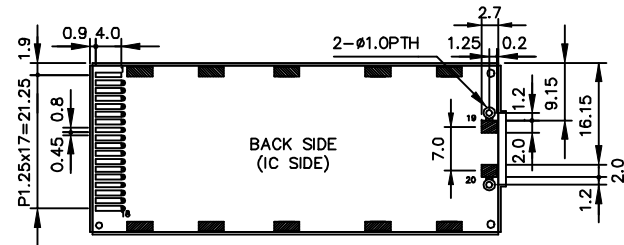
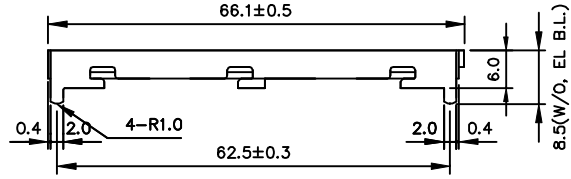


A DETAIL



INTERNAL PIN CONNECTION

PinNo.	Symbol	Level	Function
1	A $\phi$	H/L	L→INSTRUCTION H→DATA
2	CS2	L	CHIP ENABLE ACTIVE "L"
3	CS1	L	CHIP ENABLE ACTIVE "L"
4	CL	H/L	EXTERNAL CLOCK(20KHZ)
5	RD(E)	-	R $\bar{D}$ FOR 80 SERIE FOR 68 SERI
6	WR(R/W)	-	W $\bar{R}$ FOR 80 SERI,R/W FOR 68 SERI
7	VSS	-	GROUND
8	DB0	H/L	DATA BUS LINE
9	DB1	H/L	
10	DB2	H/L	
11	DB3	H/L	
12	DB4	H/L	
13	DB5	H/L	
14	DB6	H/L	
15	DB7	H/L	
16	VDD	-	POWER SUPPLY FOR LOGIC CIRCUIT
17	RES	H/L	L→80 SERIE H→68 SERIE
18	VEE	-	POWER SUPPLY FOR LCD
19	NC	-	
20	NC	-	

1. Resolution : 122X32 Dots

產品編號	LM_62_042_	南亞塑膠工業股份有限公司 NAN YA PLASTICS CORPORATION		
APPROVE	NAME	DATE	TITLE	製品圖
CHECK			DWG-NO	MX-X042X Rev.A
DESIGN				
DRAW	MAY PING	86.11.6	THIRD ANGLE PROJECT	UNIT : mm SCALE : 1.5/3